

# PC Board Layout for Floating-Point Units

National Semiconductor  
 Application Brief 40  
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 January 1989



For applications requiring floating-point capability, National Semiconductor offers two options:

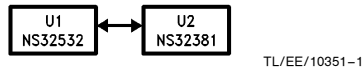
1. The NS32381: A low-cost floating-point unit (FPU) which interfaces directly to the NS32532 microprocessor.
2. The Weitek W3164: A high-performance floating-point solution which uses the NS32580 floating-point controller (FPC) to interface with the NS32532.

This application brief briefly explains how to lay-out a printed circuit (PC) board incorporating the NS32532 microprocessor and either FPU option. The board design provides maximum flexibility and can be used for either option.

**Note:** For detailed information regarding either the NS32381 FPU or the NS32580 FPC, refer to their data sheets.

The two FPU options are presented in *Figures 1* and *2*. To provide both floating-point options with minimal printed circuit board real estate, the NS32580's pin-out was designed to be fully compatible with that of the NS32381 FPU. *Figure 3* illustrates this pin compatibility and the location of the keying pins.

As a result, the layout of the PC board can be prepared using Option 2, leaving the decision for the final floating-point configuration to the user. Users who prefer Option 1, will therefore be able to insert the NS32381 into the NS32580's socket, leaving U3's socket unpopulated. This method was implemented in the VME532 board designed by National Semiconductor.



**FIGURE 1. Option 1, Using the NS32381 FPU**

**Note:** Since the NS32381's package is smaller than that of the NS32580, special care should be taken while inserting the NS32381 into the NS32580's socket.

Also, to prevent damage cause by "shifted" insertion, it is recommended that four keying-pins be installed in the NS32580 socket in the center area (see *Figure 3*).



**FIGURE 2. Option 2, Using the W3164 and NS32580 FPC**

0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	X	X	X	X	X	X	X	X	X	X	X	X	X	0	0	0	0	0	0	0	0
0	0	X	X	X	X	X	X	X	X	X	X	X	X	X	0	0	0	0	0	0	0	0
0	0	X	X	K									K	X	X	0	0	0	0	0	0	0
0	0	X	X										X	X	0	0	0	0	0	0	0	0
0	0	X	X										X	X	0	0	0	0	0	0	0	0
0	0	X	X										X	X	0	0	0	0	0	0	0	0
0	0	X	X										X	X	0	0	0	0	0	0	0	0
0	0	X	X	K									K	X	X	0	0	0	0	0	0	0
0	0	X	X	X	X	X	X	X	X	X	X	X	X	X	X	0	0	0	0	0	0	0
0	0	X	X	X	X	X	X	X	X	X	X	X	X	X	X	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

TL/EE/10351-3

x = Pins common to both NS32580 and NS32381.  
 o = Pins belonging to the NS32580 only.  
 k = Keying pins for the NS32381.

**FIGURE 3. NS32580/NS32381 Pin-Out Compatibility**

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